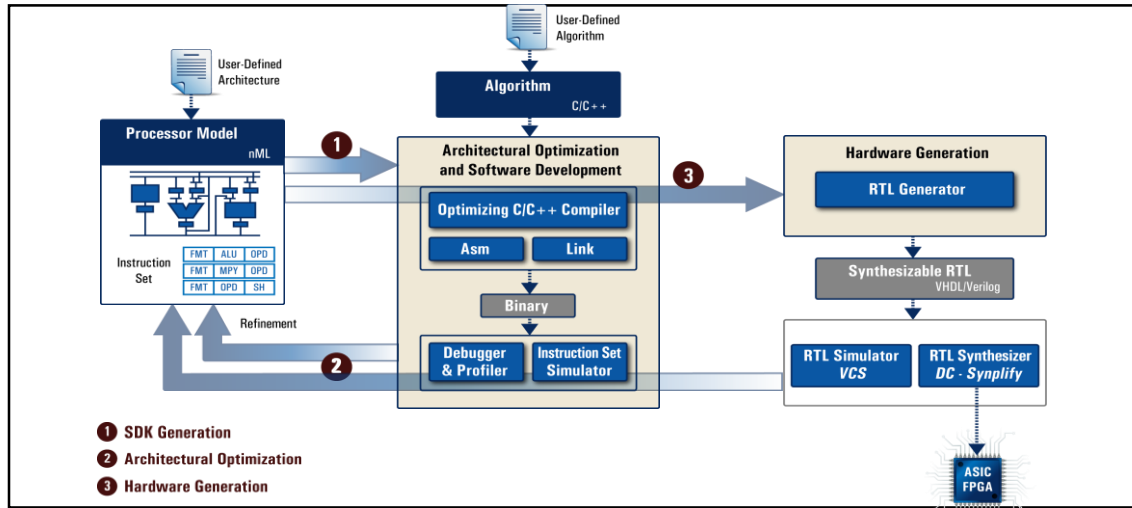


ASIP Designer: RISC-V Based Domain-Specific Processor Design Made Easy



ASIP Designer

- Design tool to accelerate the design of your own, specialized processor
- From a single processor specification, both the RTL and the full SDK (compiler, simulator, debugger) are generated
- Hundreds of tapeouts in almost all application domains (wireless, wireline, AI, automotive, medical, vision, ..)

Case Study: SHA256 Processor Design

- Accelerator for SHA256 Cryptographic Hashing
- Start from RISC-V ISA model, available in ASIP Designer's example model library
- Rapidly explore design alternative, extending the ISA and adding specialized functional unites, specialized register and memory configurations

